

N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
40	0.0088 at $V_{GS} = 10$ V	50	16 nC
	0.0105 at $V_{GS} = 4.5$ V	50	

FEATURES

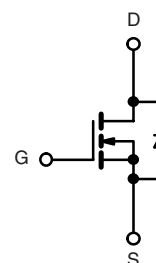
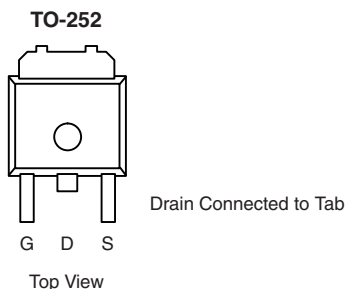
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % UIS Tested
- 100 % R_g Tested
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- LCD Display Backlight Inverters
- DC/DC Converters



Ordering Information: SUD50N04-8m8P-4GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	$T_C = 25$ °C	I_D	50 ^a	A
	$T_C = 70$ °C		44	
	$T_A = 25$ °C		14 ^b	
	$T_A = 70$ °C		11.2 ^b	
Pulsed Drain Current		I_{DM}	100	
Continuous Source-Drain Diode Current	$T_C = 25$ °C	I_S	40	A
	$T_A = 25$ °C		2.6 ^b	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	30	mJ
Avalanche Energy		E_{AS}	45	
Maximum Power Dissipation	$T_C = 25$ °C	P_D	48.1	W
	$T_C = 70$ °C		30.8	
	$T_A = 25$ °C		3.1 ^b	
	$T_A = 70$ °C		2.0 ^b	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^b	Steady State	R_{thJA}	32	40	°C/W
Maximum Junction-to-Case	Steady State	R_{thJC}	2.1	2.6	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

SPECIFICATIONS $T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted

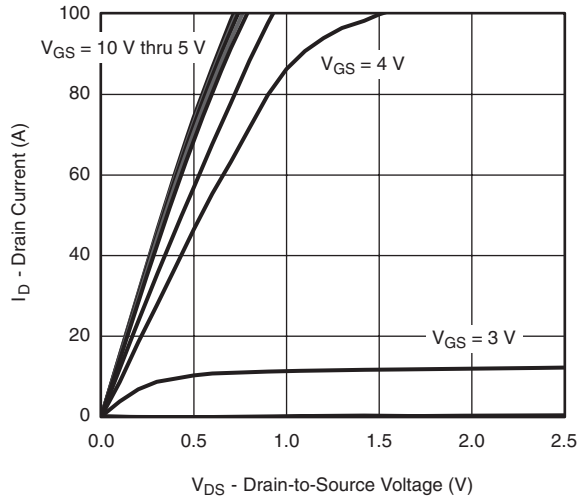
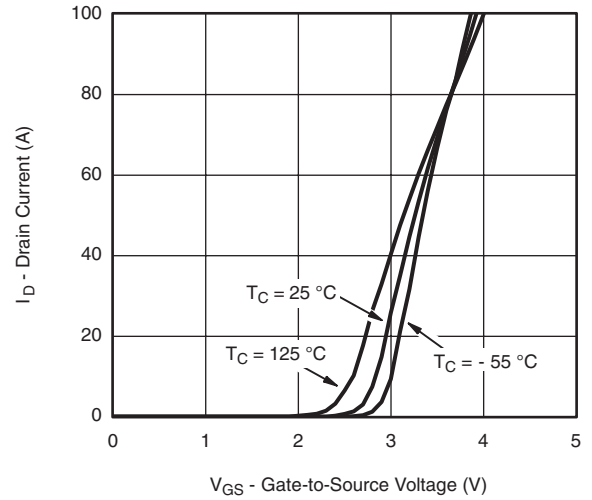
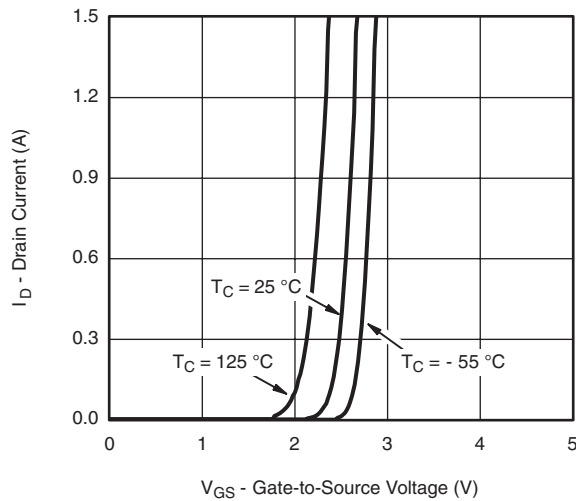
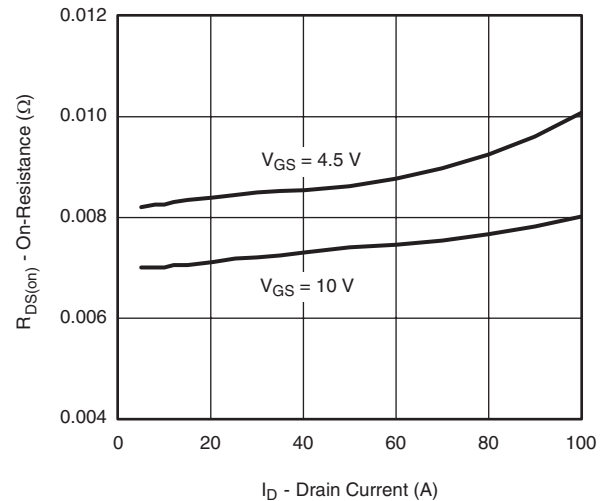
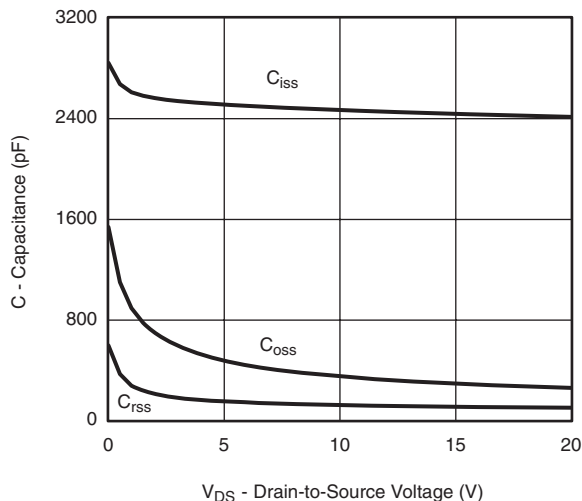
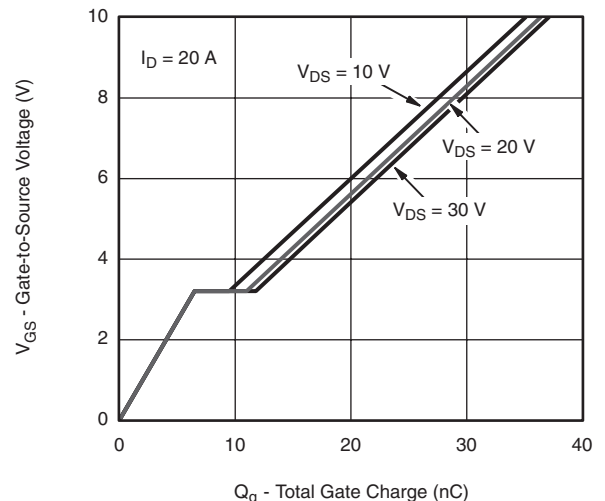
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	40			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 1.0 mA		44		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 5.9		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.5		3.0	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V			1	μA
		V _{DS} = 40 V, V _{GS} = 0 V, T _J = 70 °C			20	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	50			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.0069	0.0088	Ω
		V _{GS} = 4.5 V, I _D = 15 A		0.0084	0.0105	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 15 A		75		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 20 V, V _{GS} = 0 V, f = 1 MHz		2400		pF
Output Capacitance	C _{oss}			260		
Reverse Transfer Capacitance	C _{rss}			100		
Total Gate Charge	Q _g	V _{DS} = 20 V, V _{GS} = 10 V, I _D = 20 A		37	56	nC
		V _{DS} = 20 V, V _{GS} = 4.5 V, I _D = 20 A		16	24	
Gate-Source Charge	Q _{gs}			6.5		
Gate-Drain Charge	Q _{gd}			4.5		
Gate Resistance	R _g	f = 1 MHz	2.5	5.5	8.5	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 20 V, R _L = 1 Ω I _D ≅ 20 A, V _{GEN} = 4.5 V, R _g = 1 Ω		30	45	ns
Rise Time	t _r			15	25	
Turn-Off Delay Time	t _{d(off)}			45	70	
Fall Time	t _f			15	25	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 20 V, R _L = 1 Ω I _D ≅ 20 A, V _{GEN} = 10 V, R _g = 1 Ω		9	15	
Rise Time	t _r			5	10	
Turn-Off Delay Time	t _{d(off)}			40	60	
Fall Time	t _f			5	10	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			40	A
Pulse Diode Forward Current ^a	I _{SM}				100	
Body Diode Voltage	V _{SD}	I _S = 10 A		0.81	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 20 A, dI/dt = 100 A/μs, T _J = 25 °C		22	35	ns
Body Diode Reverse Recovery Charge	Q _{rr}			14	25	nC
Reverse Recovery Fall Time	t _a			11		ns
Reverse Recovery Rise Time	t _b			11		

Notes:

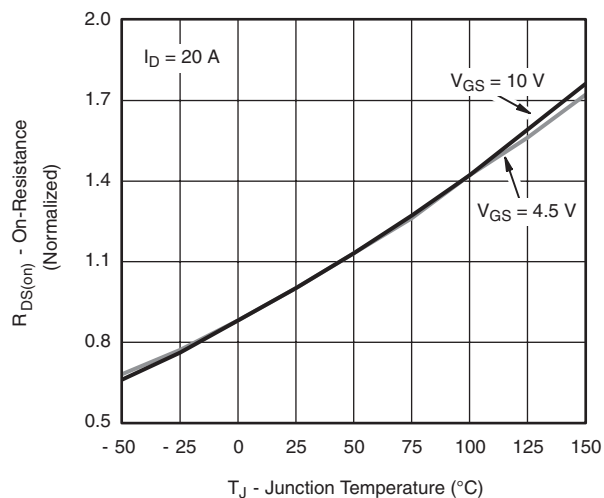
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

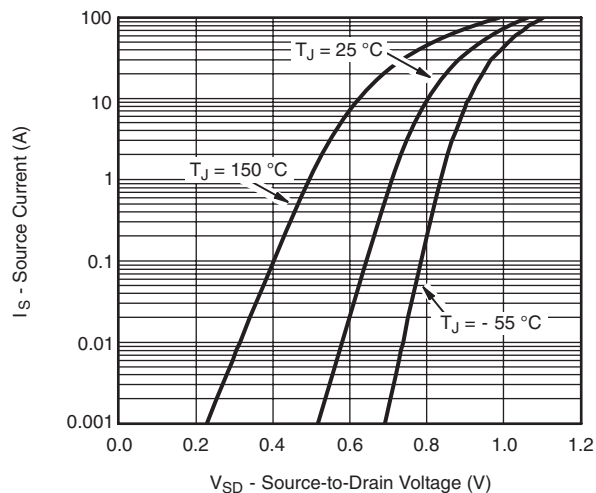
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Output Characteristics

Transfer Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

Capacitance

Gate Charge

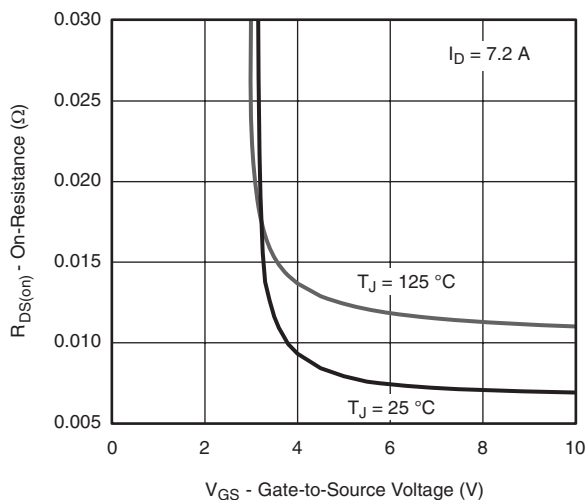
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



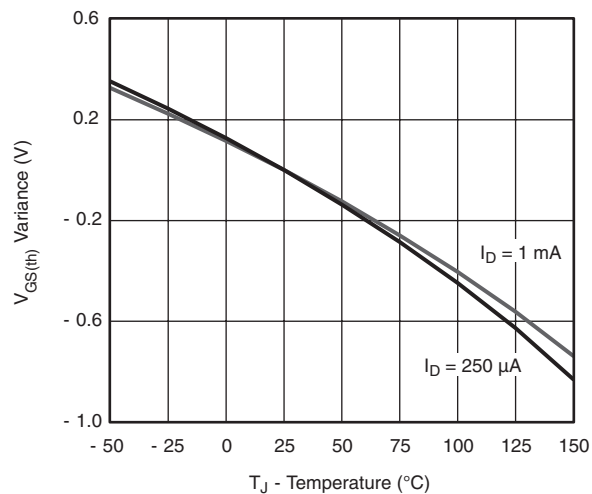
On-Resistance vs. Junction Temperature



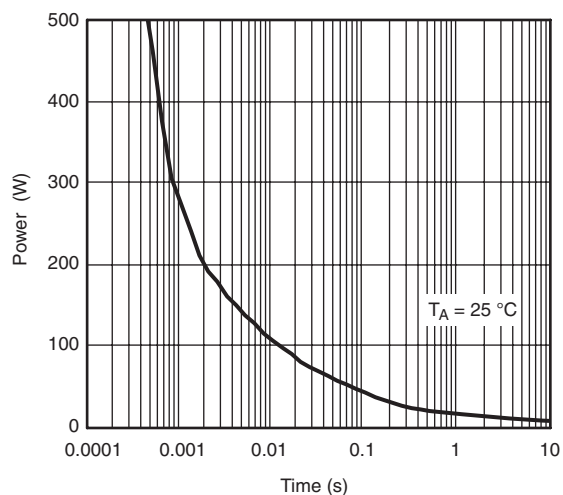
Source-Drain Diode Forward Voltage



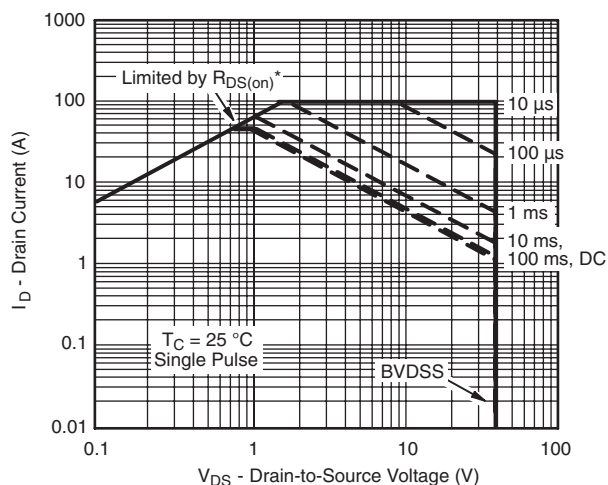
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

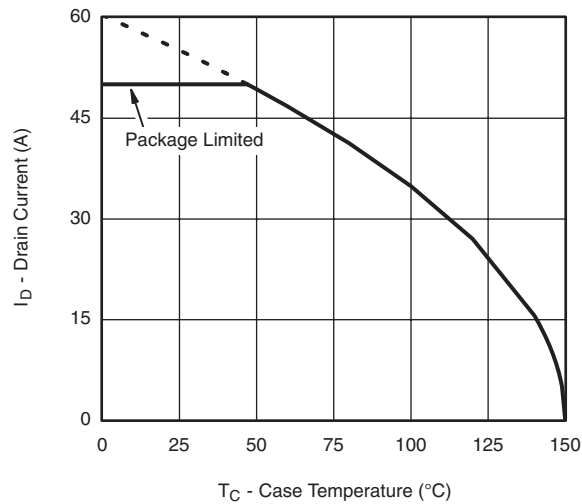


Single Pulse, Junction-to-Ambient

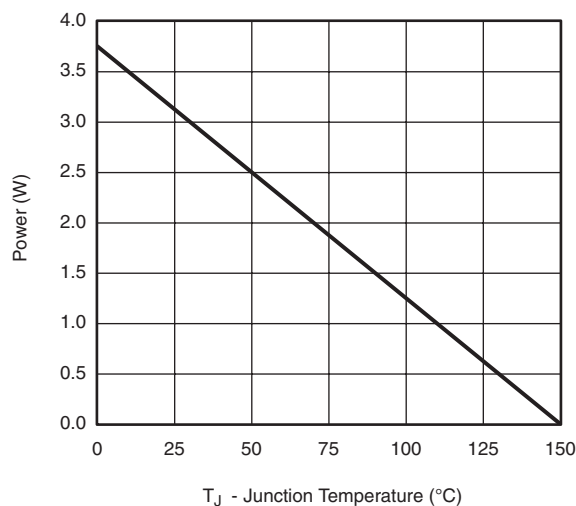


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area, Junction-to-Case

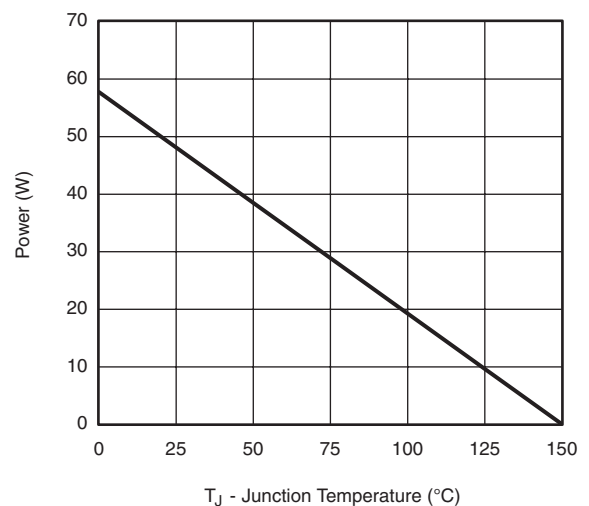
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating*, Junction-to-Case



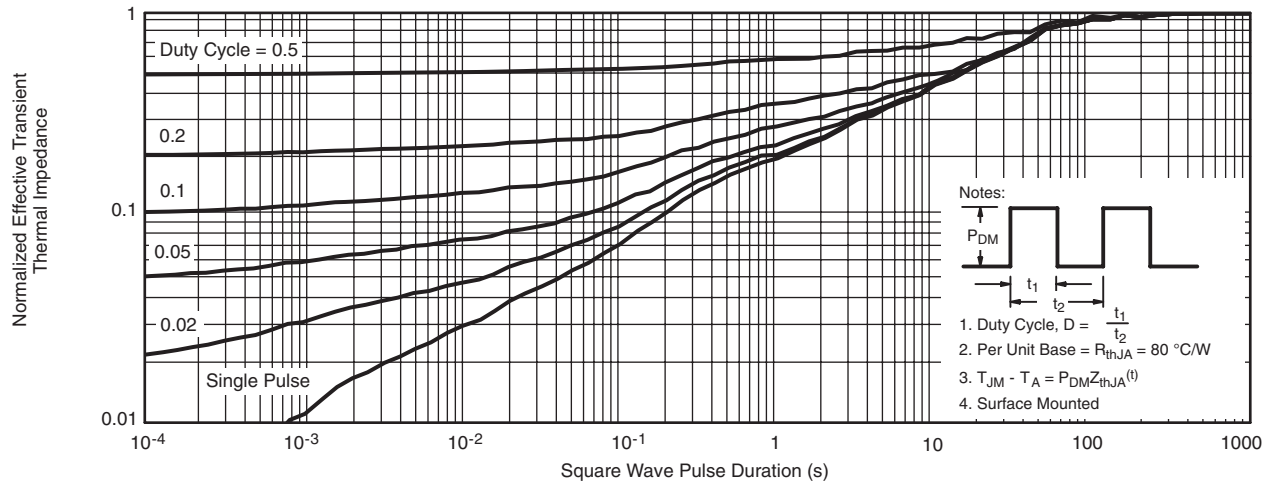
Power Derating, Junction-to-Ambient



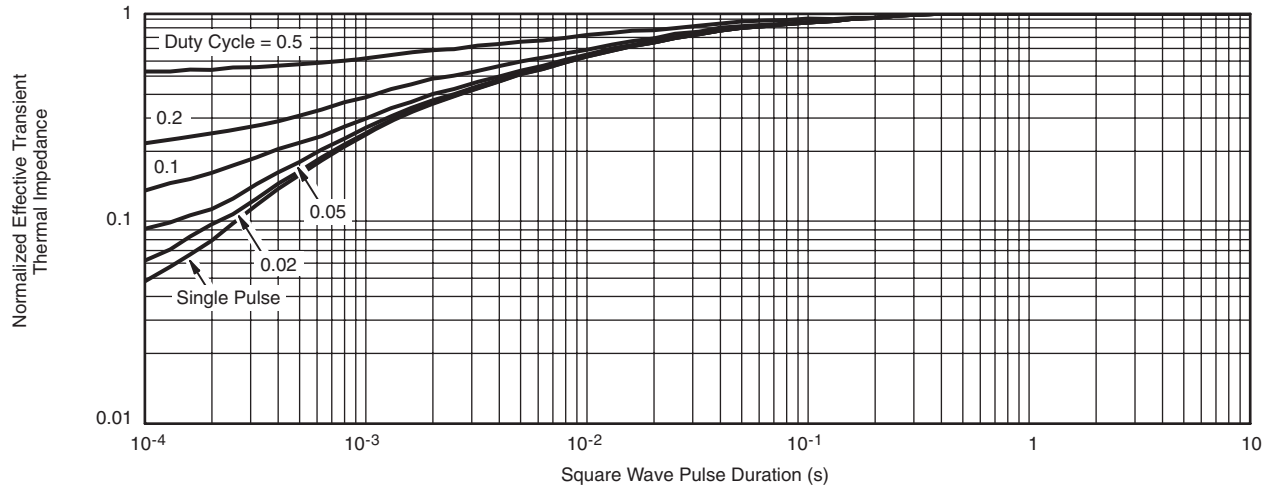
Power Derating, Junction-to-Case

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

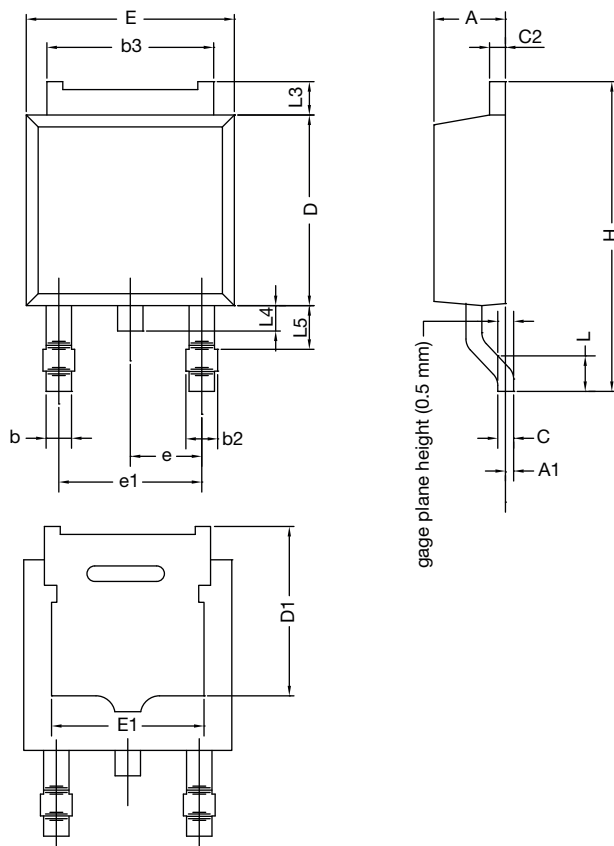


Normalized Thermal Transient Impedance, Junction-to-Case

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TO-252AA Case Outline

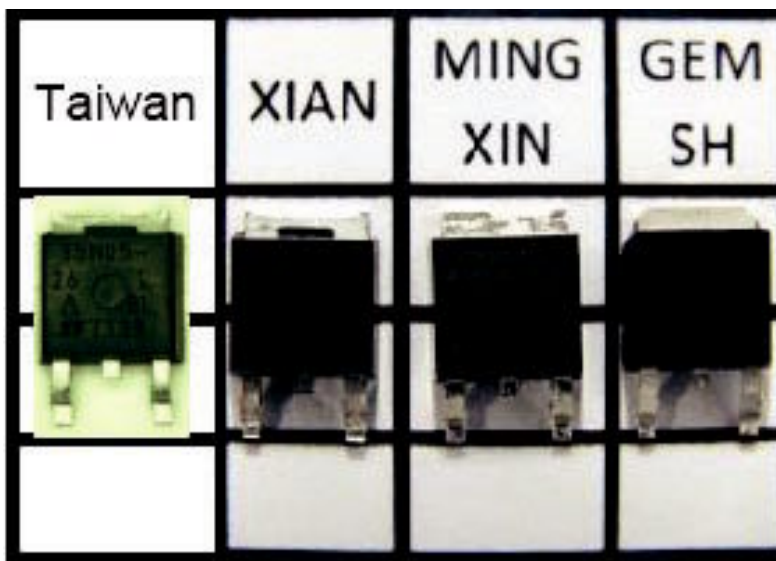


DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
C	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	4.10	-	0.161	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
H	9.40	10.41	0.370	0.410
e	2.28 BSC		0.090 BSC	
e1	4.56 BSC		0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.01	1.52	0.040	0.060

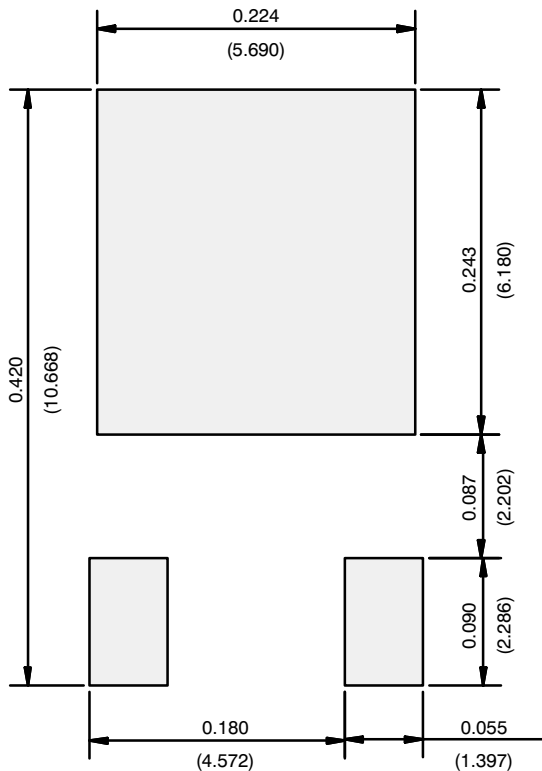
ECN: T13-0359-Rev. O, 03-Jun-13
DWG: 5347

Notes

- Dimension L3 is for reference only.
- Xi'an, Mingxin, and GEM SH actual photo.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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